## **IN THE SPECIFICATION:**

Please substitute the indicated paragraph(s) of the specification as follows:

## On Page 13, paragraph 2:

Fig. 4 provides timelines for a decryption operation that illustrates performance improvements contributed by the present invention. Section I is a timeline associated with the prior art, while Section II is a time line associated with the present invention. It should be appreciated that Section II also illustrates the effects provided by use of level sensitive latches in microcode controller system 50 during microcode fetch and execute, as disclosed in related application serial no. 09/670,892, filed September 28, 2000, and assigned to the assignee of the present application.